

R&D Engineer - IP and SoC Design - HW Security

Location: Montpellier, France

Education: Engineer (Bac +5, Universities or engineering schools)

Experience: 2 to 5 years in integrated circuit design

Date of the offer: April 2019

Context

Algodone offers a platform of software-based services and hardware blocks for digital rights management in the field of integrated circuits.

Mission

You will work in the R&D organization on tasks related to the specification, design and development of digital products on FPGA and ASIC technologies.

You will participate in customer projects and collaborative programs to define and implement new hardware architectures, developing and integrating technological bricks in the field of security: cryptography, identification, and countermeasure.

You will support the sales force and facilitate the adoption of our products through technical support.

Your curiosity, your involvement and your rigor are indispensable in this type of work to analyze and design new innovative technical solutions with high added value.

Technical skills

You have demonstrated excellence in the design of digital integrated circuits and you master:

- Architectures of SoC (System on Chip)
- ASIC and FPGA design methodologies and flows
- The rules of "Design For Reuse"

You have strong knowledge in:

- HDL Languages and Verification: VHDL, Verilog, SystemVerilog
- Front End Tools: modeling, functional verification, synthesis, DFT, formal verification
- Back End Tools: Routing Placement, DFM

You are experienced in:

- Hardware and software security
- Programming of embedded systems

Communication

You master technical English and have good oral and written communication skills.